

# N-CHANNEL GaAs HJ-FET NE6510179A

# 1 W L-BAND POWER GaAs HJ-FET

#### **DESCRIPTION**

The NE6510179A is a 1 W GaAs HJ-FET designed for middle power transmitter applications for mobile communication and wireless PC LAN systems. It is capable of delivering 1 W of output power (CW) with high linear gain, high efficiency and excellent distortion.

Reliability and performance uniformity are assured by NEC's stringent quality and control procedures.

#### **FEATURES**

· GaAs HJ-FET structure

• High output power : Pout = +31.5 dBm TYP. @Vbs = 3.5 V, Ibset = 200 mA, f = 900 MHz, Pin = +20 dBm

 $P_{out} = +32.5 \text{ dBm TYP.} @V_{DS} = 3.5 \text{ V}, I_{Dset} = 200 \text{ mA}, f = 1 900 \text{ MHz}, P_{in} = +25 \text{ dBm}$ 

Pout = +35.0 dBm TYP. @Vps = 5.0 V, Ipset = 200 mA, f = 1 900 MHz, Pin = +25 dBm

High linear gain
 G<sub>L</sub> = 15 dB TYP. @V<sub>DS</sub> = 3.5 V, I<sub>Dset</sub> = 200 mA, f = 900 MHz, P<sub>in</sub> = 0 dBm

 $G_{L}=10~dB~TYP.~@V_{DS}=3.5~V,~I_{Dset}=200~mA,~f=1~900~MHz,~P_{in}=0~dBm$ 

GL = 10 dB TYP. @VDS = 5.0 V, IDset = 200 mA, f = 1 900 MHz, Pin = 0 dBm

High power added efficiency: 70% TYP. @Vps = 3.5 V, Ibset = 200 mA, f = 900 MHz, Pin = +20 dBm

58% TYP.  $@V_{DS} = 3.5 \text{ V}$ ,  $I_{Dset} = 200 \text{ mA}$ , f = 1 900 MHz,  $P_{in} = +25 \text{ dBm}$  56% TYP.  $@V_{DS} = 5.0 \text{ V}$ ,  $I_{Dset} = 200 \text{ mA}$ , f = 1 900 MHz,  $P_{in} = +25 \text{ dBm}$ 

#### ORDERING INFORMATION

Part Number	Package	Supplying Form
NE6510179A-T1	79A	12 mm wide embossed taping     Qty 1 kpcs/reel

**Remark** To order evaluation samples, contact your nearby sales office.

Part number for sample order: NE6510179A

Caution Please handle this device at static-free workstation, because this is an electrostatic sensitive device.

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# ABSOLUTE MAXIMUM RATINGS $(T_A = +25^{\circ}C)$

Operation in excess of any one of these parameters may result in permanent damage.

Parameter	Symbol	Ratings	Unit
Drain to Source Voltage	V <sub>DS</sub>	8	V
Gate to Source Voltage	Vgso	-4	V
Drain Current	lь	2.8	Α
Gate Forward Current	<b>I</b> GF	25	mA
Gate Reverse Current	Igr	25	mA
Total Power Dissipation	Ptot	15	W
Channel Temperature	Tch	150	°C
Storage Temperature	T <sub>stg</sub>	-65 to +150	°C

## **★ RECOMMENDED OPERATING CONDITIONS**

Parameter	Symbol	Test Condition	MIN.	TYP.	MAX.	Unit
Drain to Source Voltage	Vos		-	3.5	6.0	V
Gain Compression	Gcomp		-	-	3.0	dB
Channel Temperature	Tch		-	-	+125	°C

## **ELECTRICAL CHARACTERISTICS**

 $(T_A = +25^{\circ}C, unless otherwise specified, using NEC standard test fixture.)$ 

Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Saturated Drain Current	Ipss	V <sub>DS</sub> = 2.5 V, V <sub>GS</sub> = 0 V	-	2.4	-	Α
Pinch-off Voltage	Vp	V <sub>DS</sub> = 2.5 V, I <sub>D</sub> = 14 mA	-2.0	-	-0.4	V
Gate to Drain Break Down Voltage	BV <sub>gd</sub>	I <sub>gd</sub> = 14 mA	12	-	-	V
Thermal Resistance	Rth	Channel to Case	-	5	8	°C/W
Output Power	Pout	f = 1.9 GHz, V <sub>DS</sub> = 3.5 V,	31.5	32.5	-	dBm
Drain Current	lο	$P_{in} = +25 \text{ dBm}, R_g = 100 \Omega,$	-	0.72	-	А
Power Added Efficiency	$\eta_{add}$	I <sub>Dset</sub> = 200 mA (RF OFF)	50	58	-	%
Linear Gain <sup>Note 1</sup>	G∟	Note 2	_	10.0	_	dB

**Notes 1.**  $P_{in} = 0 dBm$ 

**2.** DC performance is 100% testing. RF performance is testing several samples per wafer. Wafer rejection criteria for standard devices is 1 reject for several samples.

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## TYPICAL RF PERFORMANCE FOR REFERENCE

(TA = +25°C, unless otherwise specified, using NEC standard test fixture.)

Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Output Power	Pout	f = 900 MHz, Vps = 3.5 V,	-	31.5	-	dBm
Drain Current	lσ	$P_{in}$ = +20 dBm, $R_g$ = 100 $\Omega$ ,	-	0.53	-	Α
Power Added Efficiency	$\eta_{add}$	I <sub>Dset</sub> = 200 mA (RF OFF)	-	70	_	%
Linear Gain <sup>Note</sup>	G∟		-	15.0	_	dB

Note  $P_{in} = 0 dBm$ 

# TYPICAL RF PERFORMANCE FOR REFERENCE

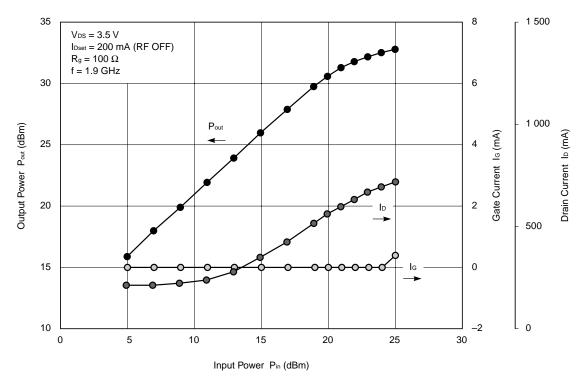
(T<sub>A</sub> = +25°C, unless otherwise specified, using NEC standard test fixture.)

Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Output Power	Pout	f = 1.9 GHz, V <sub>DS</sub> = 5.0 V,	-	35.0	-	dBm
Drain Current	lσ	$P_{in}$ = +25 dBm, $R_g$ = 100 $\Omega$ ,	-	1.2	_	Α
Power Added Efficiency	$\eta_{add}$	I <sub>Dset</sub> = 200 mA (RF OFF)	-	56	_	%
Linear Gain <sup>Note</sup>	G∟		-	10.0	_	dB

Note  $P_{in} = 0 dBm$ 

# TYPICAL CHARACTERISTICS (TA = +25°C)

## OUTPUT POWER, DRAIN CURRENT AND GATE CURRENT vs. INPUT POWER



**Remark** The graph indicates nominal characteristics.



# **S-PARAMETERS**

Test Conditions: VDS = 3.5 V, IDset = 200 mA (RF OFF)

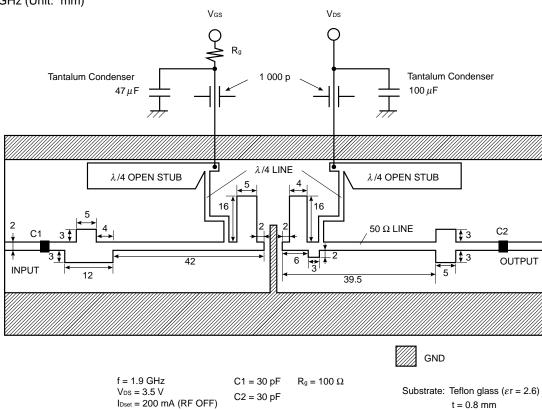
Frequency		S <sub>11</sub>		S <sub>21</sub>		<b>S</b> 12		S <sub>22</sub>
MHz	MAG.	ANG. (deg.)	MAG.	ANG. (deg.)	MAG.	ANG. (deg.)	MAG.	ANG. (deg.)
550	0.940	179.9	2.859	92.3	0.020	21.9	0.803	177.2
600	0.938	178.6	2.699	92.6	0.020	24.3	0.808	177.2
650	0.943	177.5	2.440	91.9	0.020	25.3	0.806	176.5
700	0.940	176.9	2.316	91.5	0.021	26.7	0.807	176.3
750	0.939	175.8	2.098	91.2	0.020	28.0	0.802	175.3
800	0.948	175.1	2.008	90.3	0.021	29.6	0.796	175.5
850	0.949	174.9	1.826	92.0	0.020	31.2	0.802	173.7
900	0.938	174.1	1.772	90.5	0.022	32.0	0.804	174.5
950	0.940	173.2	1.691	92.5	0.020	36.4	0.808	173.7
1000	0.940	172.4	1.579	90.2	0.022	34.6	0.807	173.5
1050	0.940	171.9	1.545	92.7	0.021	39.3	0.806	172.8
1100	0.938	171.7	1.447	91.4	0.022	37.3	0.805	172.3
1150	0.940	171.1	1.432	91.0	0.022	40.5	0.807	171.9
1200	0.936	170.4	1.342	90.6	0.023	40.4	0.803	171.4
1250	0.935	170.1	1.325	91.3	0.022	42.5	0.803	171.3
1300	0.935	169.4	1.224	89.2	0.023	42.3	0.800	170.8
1350	0.933	169.9	1.232	90.2	0.023	47.0	0.806	171.4
1400	0.934	168.0	1.140	90.0	0.023	45.5	0.795	169.9
1450	0.937	167.5	1.105	88.5	0.024	46.1	0.804	169.5
1500	0.938	167.0	1.075	90.0	0.023	49.5	0.801	168.8
1550	0.938	166.4	1.045	90.3	0.024	49.6	0.803	168.7
1600	0.934	167.1	1.019	91.1	0.024	52.3	0.803	169.5
1650	0.936	165.1	0.997	89.7	0.025	53.3	0.803	167.6
1700	0.933	164.6	0.982	91.0	0.025	54.8	0.800	166.7
1750	0.934	163.7	0.936	89.6	0.026	54.6	0.803	166.6
1800	0.934	162.9	0.961	87.4	0.026	57.7	0.799	165.3
1850	0.934	162.1	0.905	90.9	0.026	55.6	0.804	165.4
1900	0.935	161.7	0.875	85.8	0.028	58.5	0.798	164.1
1950	0.931	160.7	0.855	89.3	0.026	58.2	0.800	164.1
2000	0.929	160.4	0.807	87.2	0.028	59.5	0.797	163.2
2050	0.929	158.8	0.796	89.3	0.027	58.0	0.802	162.6
2100	0.933	158.1	0.772	85.5	0.028	63.2	0.797	161.1
2150	0.928	156.6	0.769	92.6	0.027	60.2	0.792	160.5
2200	0.934	156.5	0.729	87.0	0.028	65.2	0.793	159.9
2250	0.930	155.5	0.716	92.2	0.027	65.6	0.795	159.9

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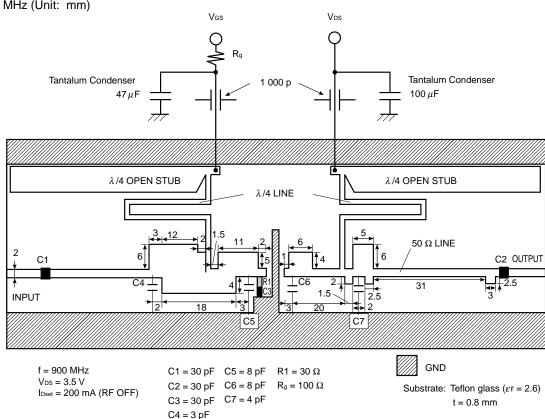
## APPLICATION CIRCUIT EXAMPLE

f = 1.9 GHz (Unit: mm)



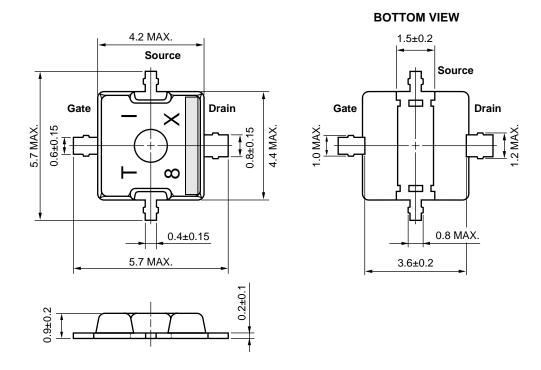
# **APPLICATION CIRCUIT EXAMPLE**

f = 900 MHz (Unit: mm)

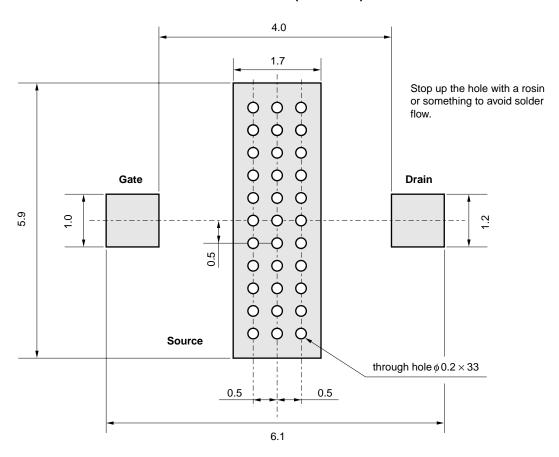


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# 79A PACKAGE DIMENSIONS (Unit: mm)



# 79A PACKAGE RECOMMENDED P.C.B. LAYOUT (Unit: mm)





## RECOMMENDED SOLDERING CONDITIONS

This product should be soldered and mounted under the following recommended conditions. For soldering methods and conditions other than those recommended below, contact your nearby sales office.

Soldering Method	Soldering Conditions	Recommended Condition Symbol
Infrared Reflow	Package peak temperature: 235°C or below, Time: 30 seconds or less (at 210°C or higher), Count: 2 times or less, Exposure limit: None <sup>Note</sup>	IR35-00-2
Partial Heating	Pin temperature: 260°C or below, Time: 5 seconds or less (per pin row), Exposure limit: None <sup>Note</sup>	_

**Note** After opening the dry pack, store it at 25°C or less and 65% RH or less for the allowable storage period.

Caution Do not use different soldering methods together (except for partial heating).

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M8E 00.4-0110



## SAFETY INFORMATION ON THIS PRODUCT

#### Caution

**GaAs Products** 

The product contains gallium arsenide, GaAs.

GaAs vapor and powder are hazardous to human health if inhaled or ingested.

- · Do not destroy or burn the product.
- Do not cut or cleave off any part of the product.
- · Do not crush or chemically dissolve the product.
- Do not put the product in the mouth.

Follow related laws and ordinances for disposal. The product should be excluded from general industrial waste or household garbage.

#### ▶Business issue

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#### ▶ Technical issue

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