

TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

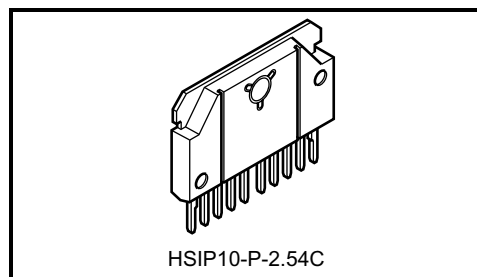
# TA8265K

## Dual Audio Power Amplifier

TA8265K is a high-output audio power IC developed for outputting audio signals for TV and compact stereos. Fewer external components and a sealed compact 10-pin package means the IC needs only a small space on the printed circuit board. The IC incorporates thermal shutdown and load short-protection circuits.

### Features

- High output power:  $P_{out} = 6 \text{ W/channel (Typ.)}$   
( $V_{CC} = 20 \text{ V}$ ,  $R_L = 8 \Omega$ ,  $f = 1 \text{ kHz}$ ,  $THD = 10\%$ )
- Low noise:  $V_{no} = 0.14 \text{ mVrms (Typ.)}$   
( $V_{CC} = 20 \text{ V}$ ,  $R_L = 8 \Omega$ ,  $GV = 34\text{dB}$ ,  $R_g = 10 \text{ k}\Omega$ ,  $BW = 20 \text{ Hz} \sim 20 \text{ kHz}$ )
- Very few external parts
- Built in thermal shut down protector circuit
- Operating supply voltage range:  $V_{CC (opr)} = 10 \sim 30 \text{ V}$  ( $T_a = 25^\circ\text{C}$ )



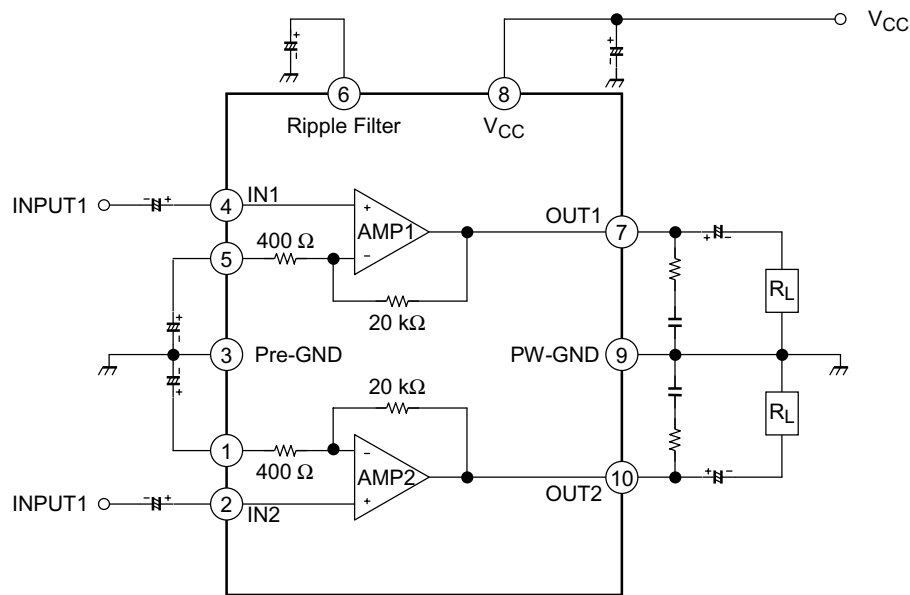
HSIP10-P-2.54C

Weight: 3.15 g (Typ.)

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## Block Diagram



## Application Information

### Voltage gain

The closed loop voltage gain is determined by R<sub>1</sub>, R<sub>2</sub>.

$$G_V = 20 \log \frac{R_1 + R_2}{R_2} \text{ (dB)}$$

$$= 20 \log \frac{20 \text{ k}\Omega + 400 \Omega}{400 \Omega}$$

$$\approx 34 \text{ (dB)}$$

- (a) Amplifier with gain  $G_V < 34 \text{ (dB)}$

$$G_V = 20 \log \frac{R_1 + R_2 + R_4}{R_2 + R_4} \text{ (dB)}$$

When  $R_4 = 220 \Omega$

$$G_V \approx 30 \text{ (dB)}$$

is gain.

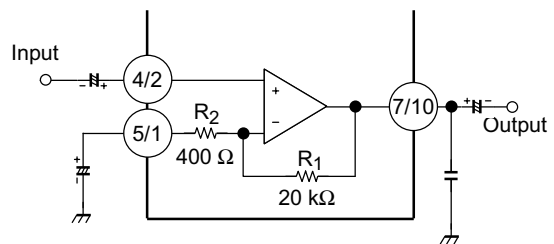


Figure 1

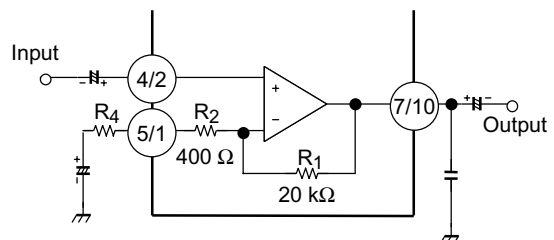
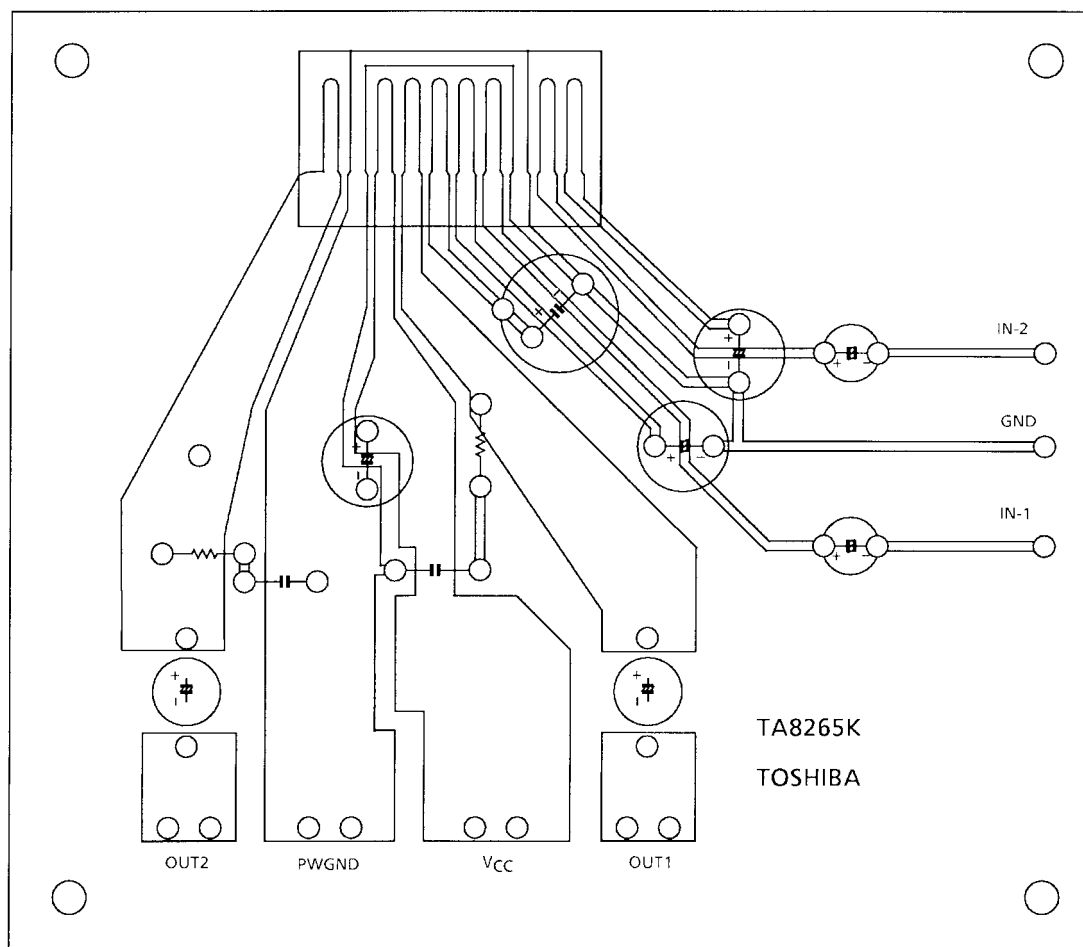


Figure 2

## Cautions

This IC is not proof enough against a strong E-M field by CRT which may cause malfunction such as leak. Please set the IC keeping the distance from CRT.

## Standard P.C.B



(Bottom view)

**Maximum Ratings (Ta = 25°C)**

Characteristic	Symbol	Rating	Unit
Supply voltage	V <sub>CC</sub>	30	V
Output current (peak/ch)	I <sub>O</sub> (peak)	2	A
Power dissipation	P <sub>D</sub> (Note)	20	W
Operating temperature	T <sub>opr</sub>	-20~75	°C
Storage temperature	T <sub>stg</sub>	-55~150	°C

Note: Derated above Ta = 25°C in the proportion of 267 mW/°C.

**Electrical Characteristics**

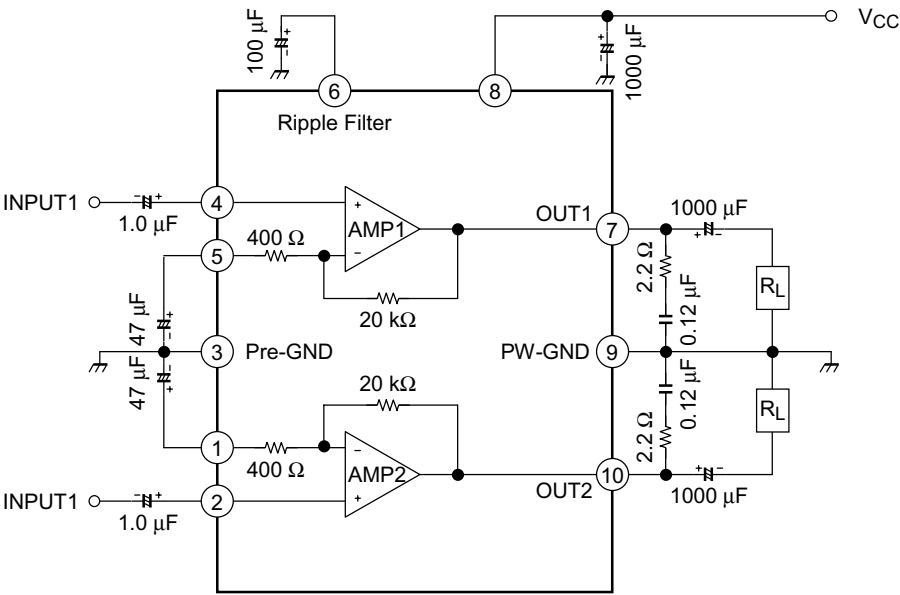
(Unless otherwise specified, V<sub>CC</sub> = 20 V, R<sub>L</sub> = 8 Ω, R<sub>g</sub> = 600 Ω, f = 1 kHz, Ta = 25°C)

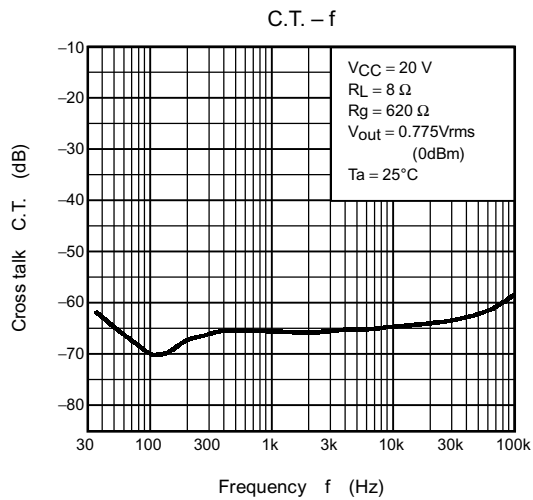
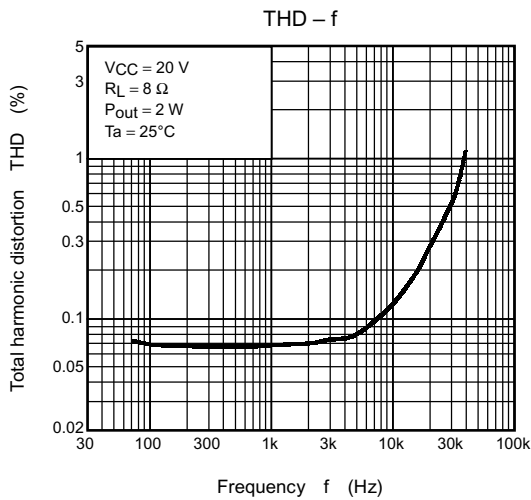
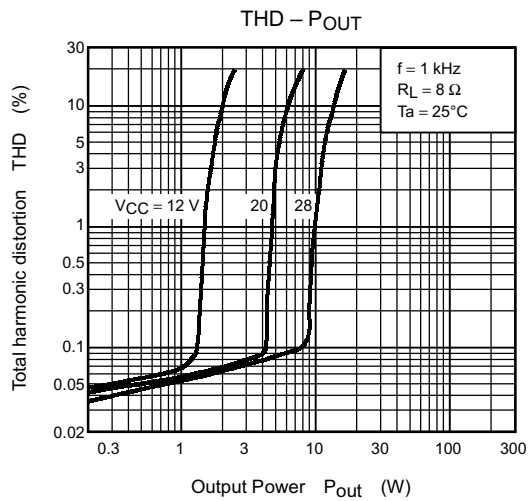
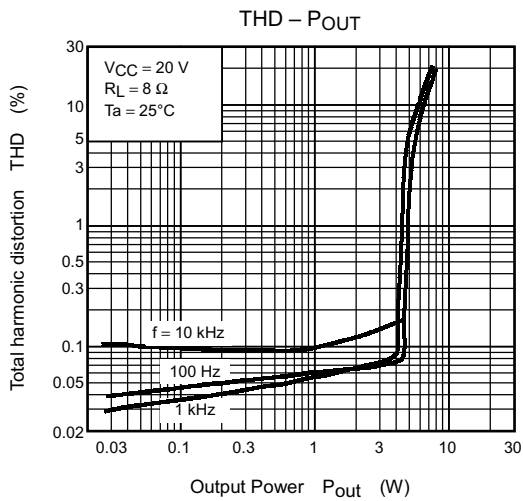
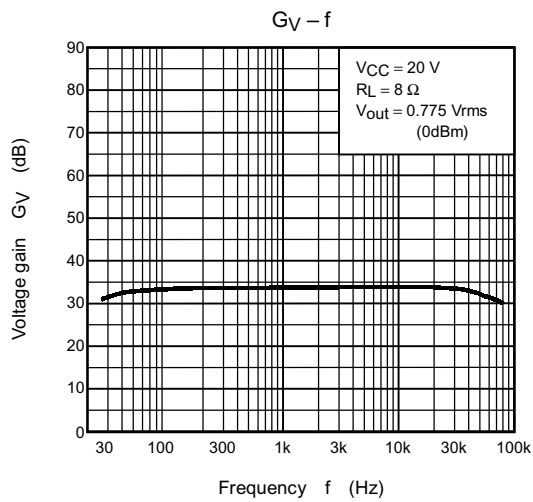
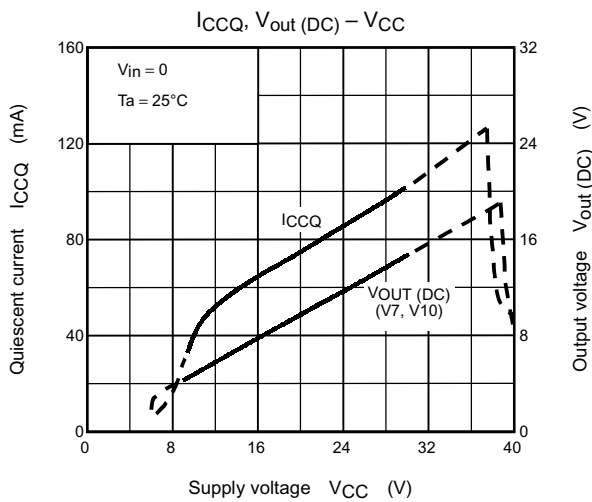
Characteristic	Symbol	Test Circuit	Test Condition	Min.	Typ.	Max	Unit
Quiescent current	I <sub>CCQ</sub>	—	V <sub>in</sub> = 0	—	75	130	mA
Output power	P <sub>out</sub> (1)	—	THD = 10%	5.0	6.0	—	W
	P <sub>out</sub> (2)	—	THD = 1%	—	4.5	—	
Total harmonic distortion	THD	—	P <sub>out</sub> = 2 W	—	0.1	0.6	%
Closed loop voltage gain	G <sub>V</sub>	—	V <sub>out</sub> = 0.775 V <sub>rms</sub> (0dBm)	32.5	34.0	35.5	dB
Cross talk	C.T.	—	V <sub>out</sub> = 0.775 V <sub>rms</sub> (0dBm)	—	-65	—	dB
Input resistance	R <sub>IN</sub>	—	—	—	30	—	kΩ
Ripple rejection ratio	R.R.	—	R <sub>g</sub> = 10 kΩ, f <sub>ripple</sub> = 100 Hz V <sub>ripple</sub> = 0.775 V <sub>rms</sub> (0dBm)	-45	-57	—	dB
Output noise voltage	V <sub>no</sub>	—	R <sub>g</sub> = 10 kΩ, BW = 20 Hz~20 kHz	—	0.14	0.3	mV <sub>rms</sub>

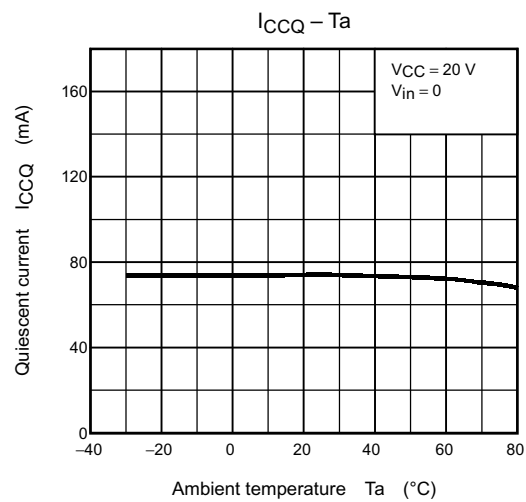
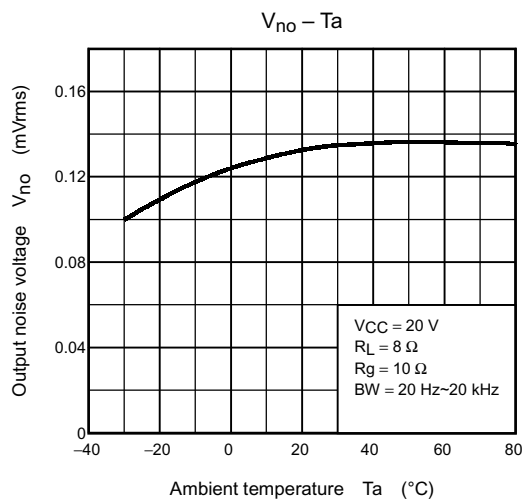
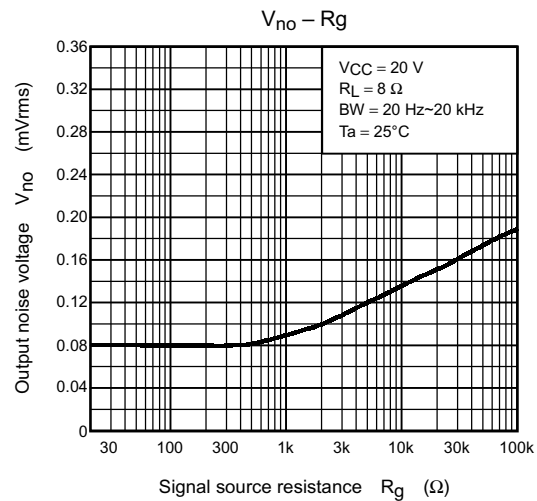
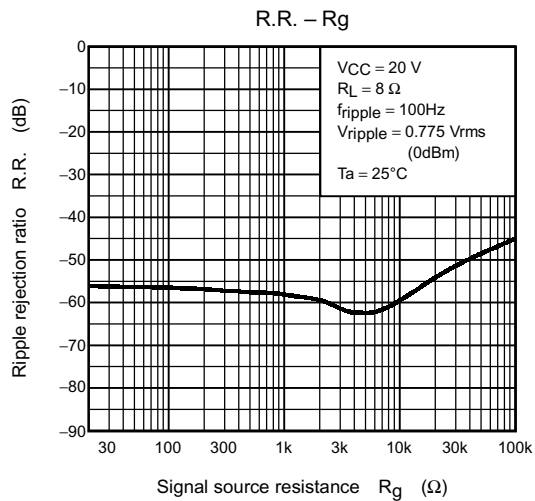
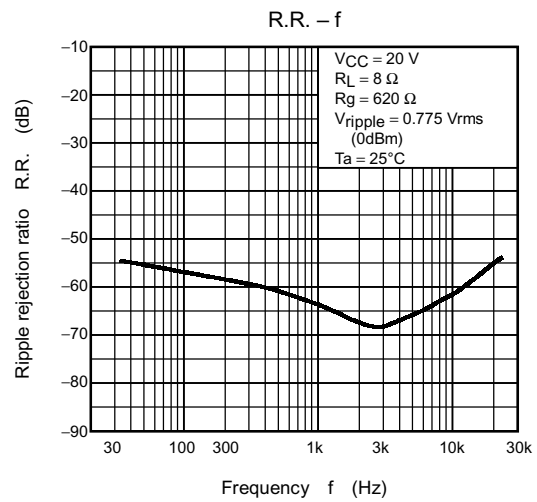
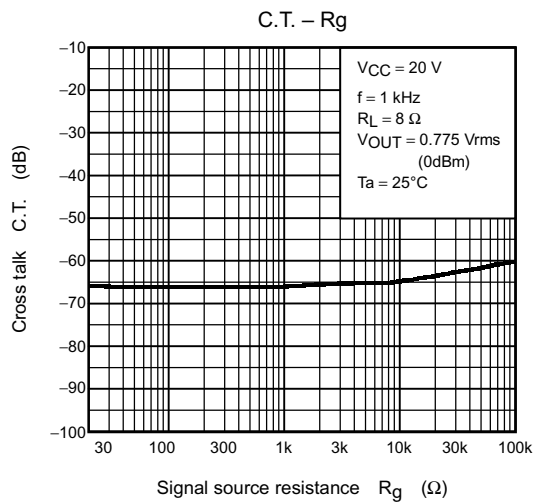
**Typ. DC Voltage of Each Terminal (V<sub>CC</sub> = 20 V, Ta = 25°C)**

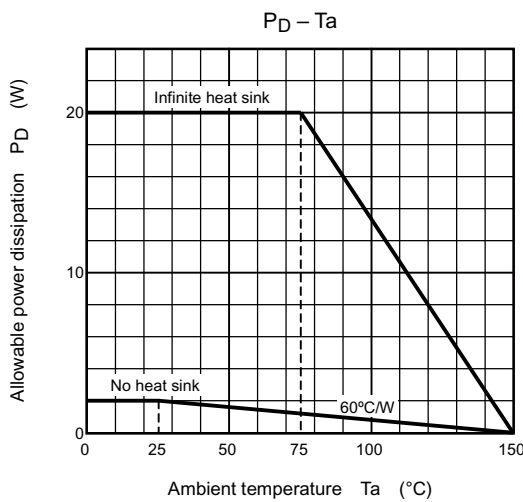
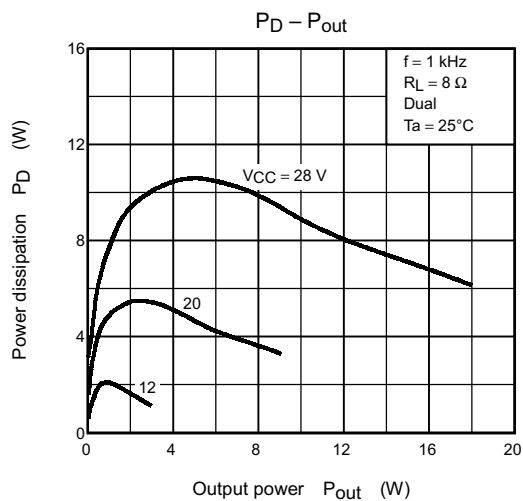
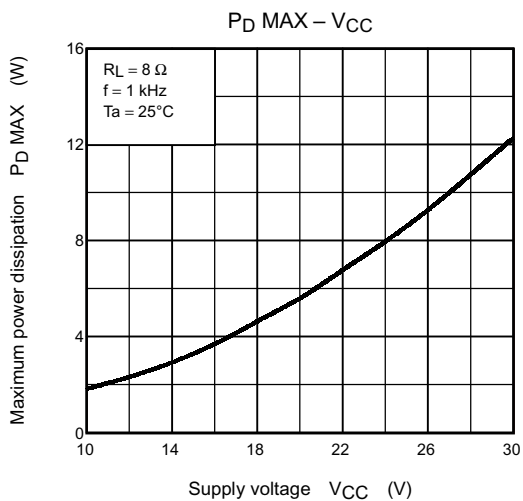
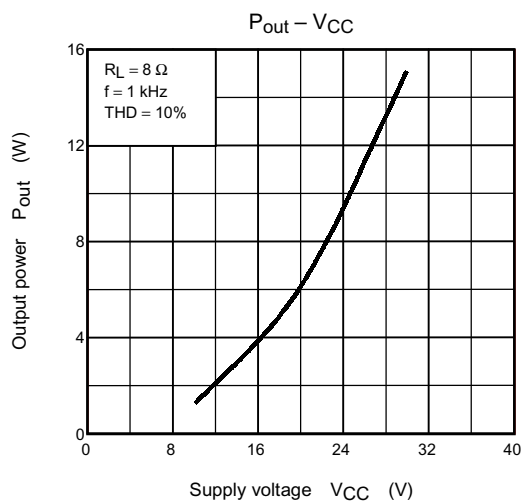
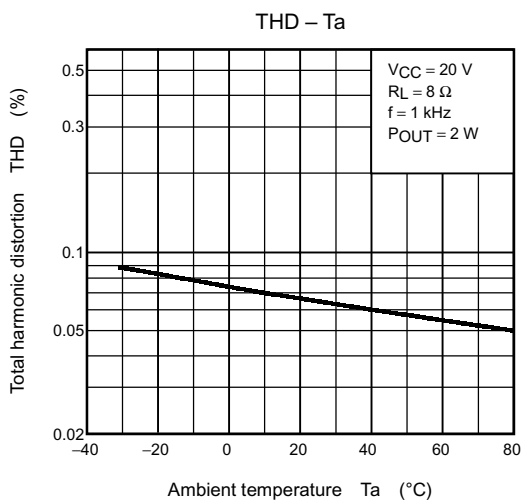
Terminal No.	1	2	3	4	5	6	7	8	9	10
DC voltage (V)	2.1	2.25	GND	2.25	2.1	6.8	9.8	V <sub>CC</sub>	GND	9.8

Test Circuit











## HSIP10-P-2.54C

Unit : mm

